

PTO/SB/33 (07-05)

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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional) P16828		
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)]	Application Number Filed: September 30, 2003			
on September 6, 2005 Signature	First Named Inventor: Hyunjun Kim			
Typed or printed nameEdith Martin	Art Unit 284	1 .	Examiner: Norris, Jeremy C.	
Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.				
This request is being filed with a notice of appeal.				
The review is requested for the reason(s) stated on the attached sheet(s): Note: No more than five (5) pages may be provided.				
I am the applicant/inventor. assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)	Patrick	J. Buckley	Such Oy gnature	
attorney or agent of record. Registration number40,928	_(203):	972-0191 Teleph	one number	
attorney or agent acting under 37 CFR 1.34. Registration number if acting under 37 CFR 1.34	_Septe	mber 6, 2005	Date	
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.				
×Total of _3 forms are submitted.				

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.





FILED UNDER 37 CFR 1.116 EXPEDITED PROCEDURE EXAMINING GROUP ART UNIT 2841

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

) Group Art Unit: 2841
Applicants: KIM et al.) Examiner: Norris, Jeremy C.) Reason(s) for Requesting a Pre-Appeal
Application Serial No.: 10/674,886) Brief Review
Filing Date: September 30, 2003	Attorney Docket No.: P16828
For: FLOATING TRACE ON SIGNAL LAYER	PTO Customer Number 28062 Buckley, Maschoff & Talwalkar LLC Five Elm Street New Canaan, CT 06840)

CERTIFICATE OF MAILING UNDER 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed tα Mail Stop After Final, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 6, 2005.

Dated: September 6, 2005 By:

Mail Stop After Final Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants respectfully request a Pre-Appeal Brief Review for the reason(s) that begin on page 2 of this paper.

REASON(S) FOR REQUESTING A PRE-APPEAL BRIEF REVIEW

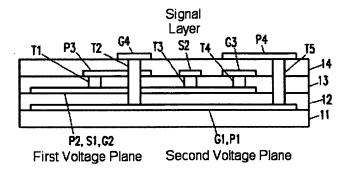
The pending claims stand finally rejected under 35 USC 102 and/or 103 as being unpatentable in view of US Patent No. 6,172,305 ("Tanahashi"). Because Tanahashi fails to disclose a feature recited in all of the pending claims, reversal of the final rejection is respectfully requested.

The pending claims recite "a first voltage plane," "a signal layer on one side of the first voltage plane," and "a second voltage plane on the other side of the first voltage plane." That is, the signal layer and the second voltage plane are on opposite sides of the first voltage plane. Moreover, the claims recite "a plurality of floating microstrip line traces on the signal layer, wherein each microstrip line is ... electrically connected to the <u>second</u> voltage plane."

According to the Office Action, Tanahashi discloses in FIGS. 1A through 1C:

- a first voltage plane (12, comprising P2, S1, G2);
- a signal layer (13, comprising S2); and
- a second voltage plane (11, comprising G1, P1).

Thus, labeling FIG. 1B of Tanahashi:



While the signal layer and the second voltage plane are on opposite sides of the first voltage plane (as recited in the pending claims), P3 and G3 (treated as microstrip lines in the Office Action) are not "electrically connected to the <u>second</u> voltage plane" as recited in the pending claims. Rather, T1 and T4 electrically connect P3 and G3 to what is treated as the <u>first</u> voltage plane in the Office Action.

Application Serial No.: 10/674,886 Reason(s) for Requesting a Pre-Appeal Brief Review

Moreover, Applicants respectfully suggest that the other embodiments disclosed in Tanahashi suffer from a similar deficiency.

Accordingly, Applicants respectfully request that the final rejection be reversed. If any issues remain, or if the Examiner has any further suggestions for expediting allowance of the present application, the Examiner is kindly invited to contact the undersigned via telephone at (203) 972-0191.

Respectfully submitted,

September 6, 2005

Date

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